



1/8

Approved  
TP  
4/30/03

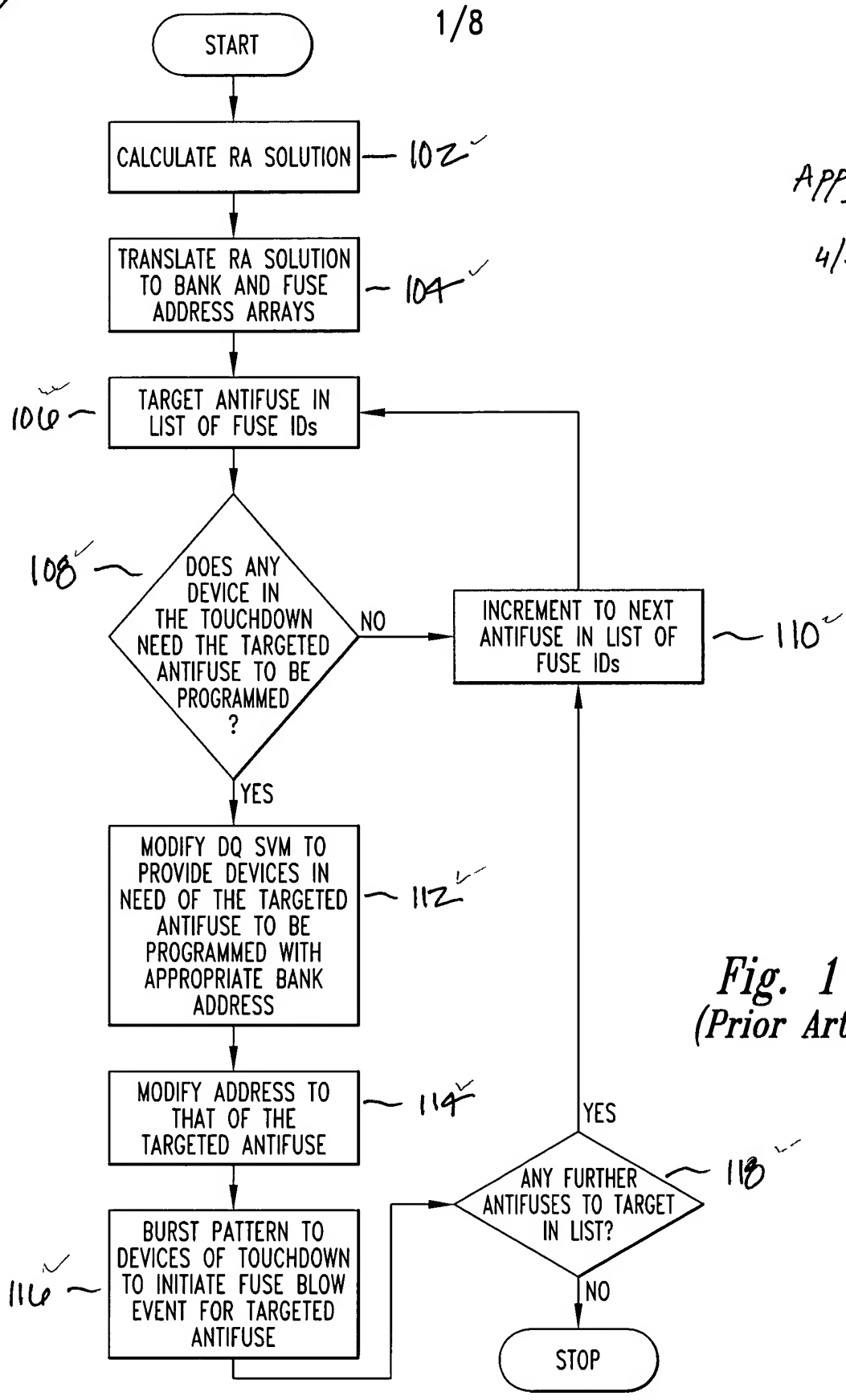


Fig. 1  
(Prior Art)

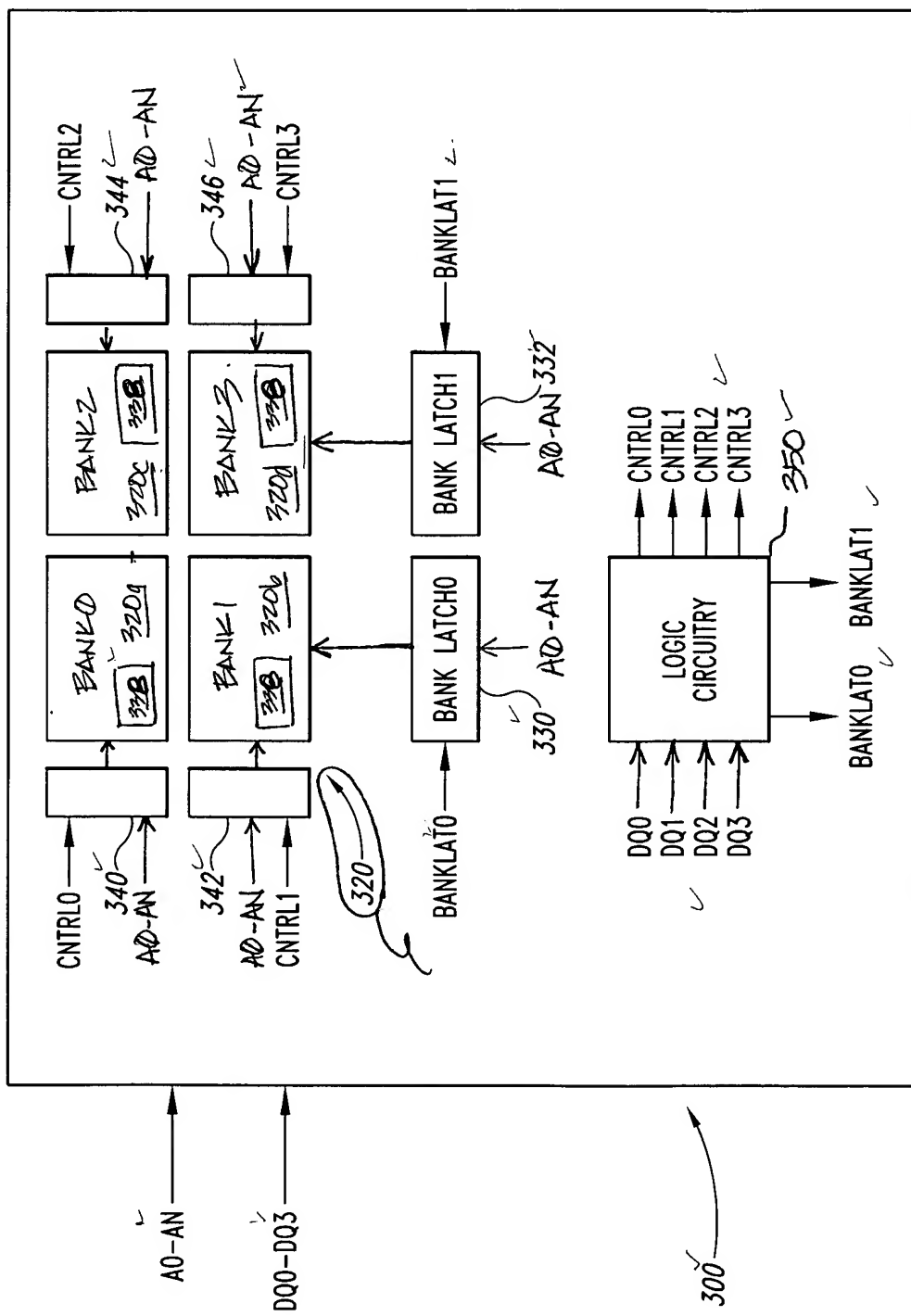


Fig. 2



3/8

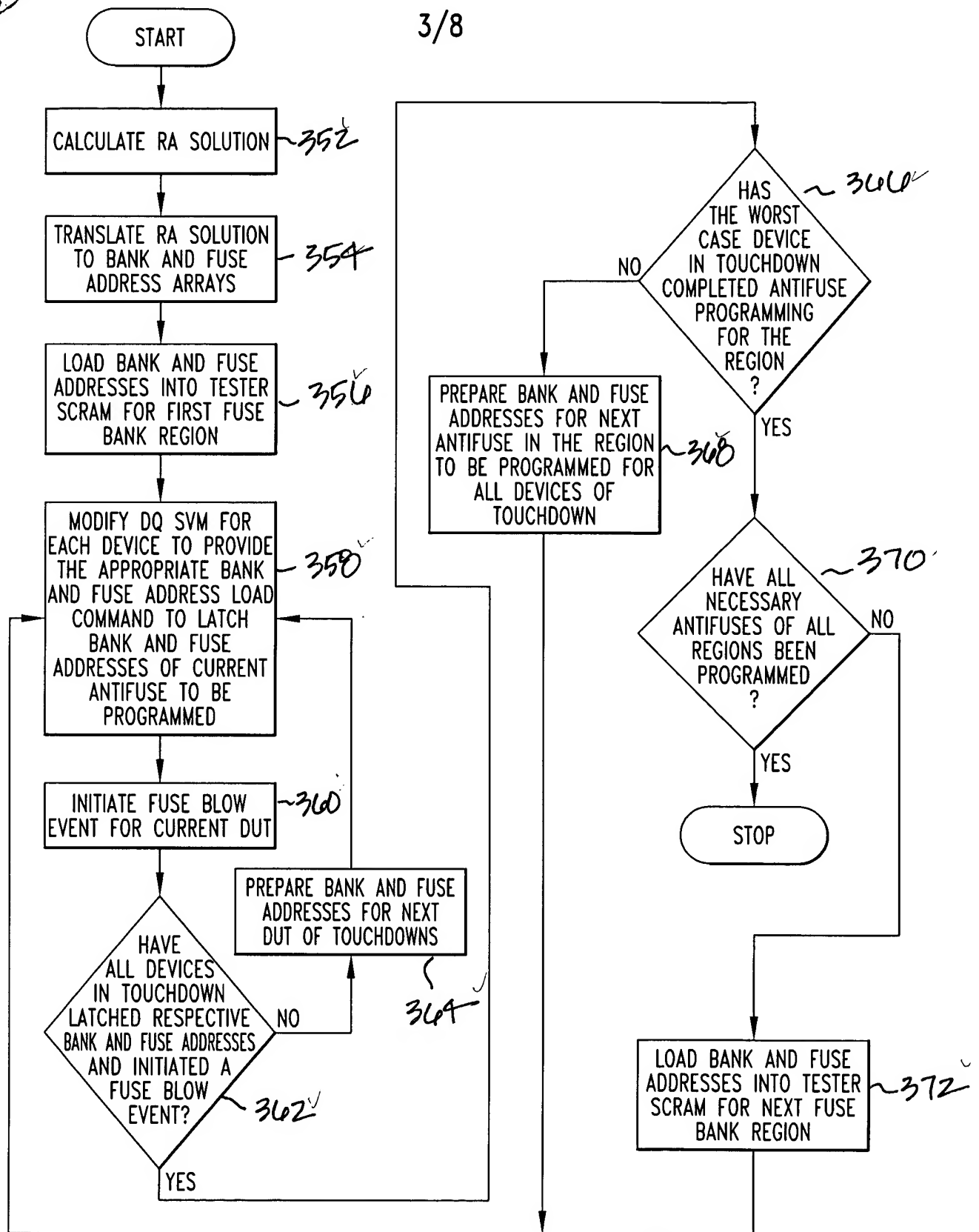
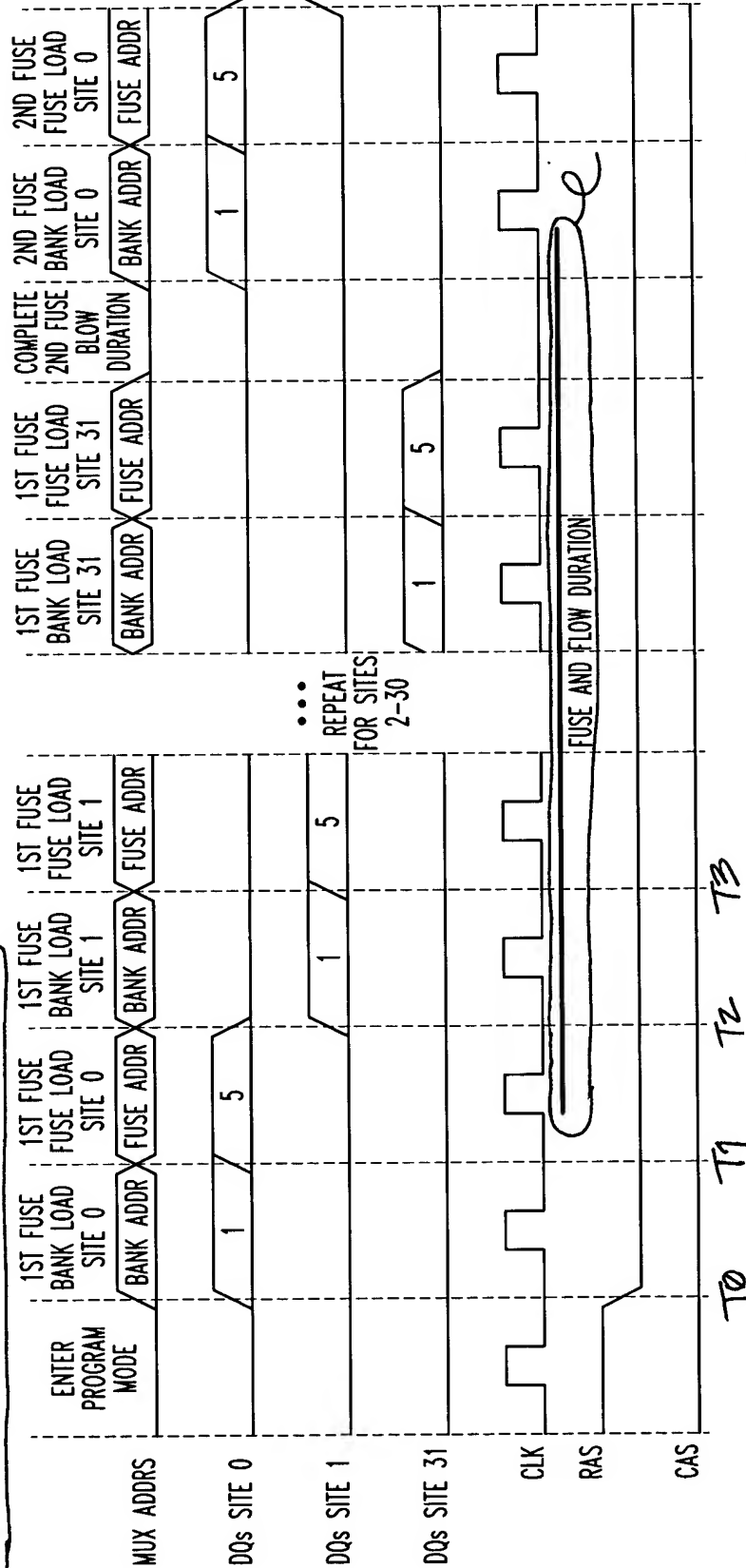


Fig. 3

**SIMPLIFIED WAVEFORM FOR J994 FUSE BLOW METHOD**



...  
REPEAT  
FOR ALL  
FUSES IN  
REGION

4/8

DQs FOR BANK AND FUSE ADDRESS LOADING

3210	HEX	FUNCTION
0000	0	LOAD NOTHING
0001	1	LOAD BANK ADDRESS LEFT
0010	2	LOAD BANK ADDRESS RIGHT
0101	5	LOAD FUSE ADDRESS TOP LEFT
0110	6	LOAD FUSE ADDRESS TOP RIGHT
1001	9	LOAD FUSE ADDRESS BOTTOM LEFT
1010	A	LOAD FUSE ADDRESS BOTTOM RIGHT

Fig. 4



5/8

500

SCRAM RAM		DQ SVM DATA					
		DUT 0 0123	DUT 1 0123	DUT 1 0123	DUT 31 0123	DUT 31 0123	DUT 63 0123
502	BANK ADDR DUT 0	1000	0000	...	0000	...	500
504	FUSE ADDR DUT 0	1010	0000	...	0000	...	
502	BANK ADDR DUT 1	0000	1000	...	0000	...	
504	FUSE ADDR DUT 1	0000	1010	...	0000	...	
502	BANK ADDR DUT 31	0000	0000	...	1000	...	0000
504	FUSE ADDR DUT 31	0000	0000	...	1010	...	
502	BANK ADDR DUT 63	0000	0000	...	0000	...	1000
504	FUSE ADDR DUT 63	0000	0000	...	0000	...	

Fig. 5



6/8

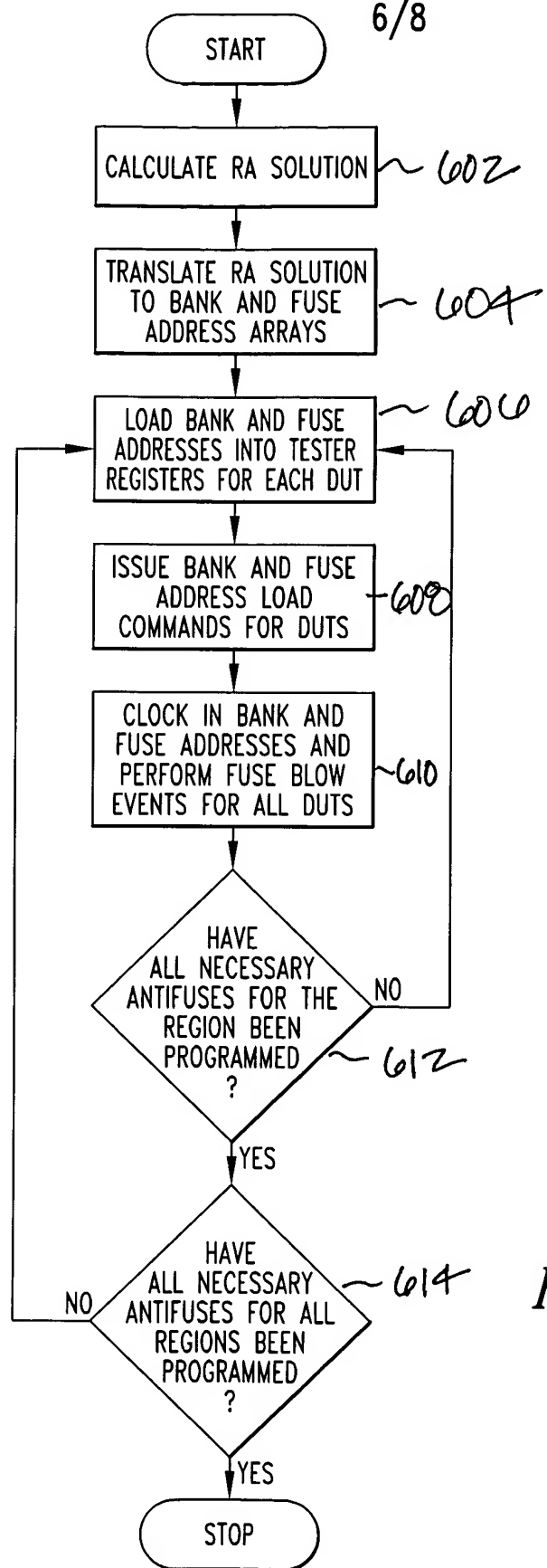
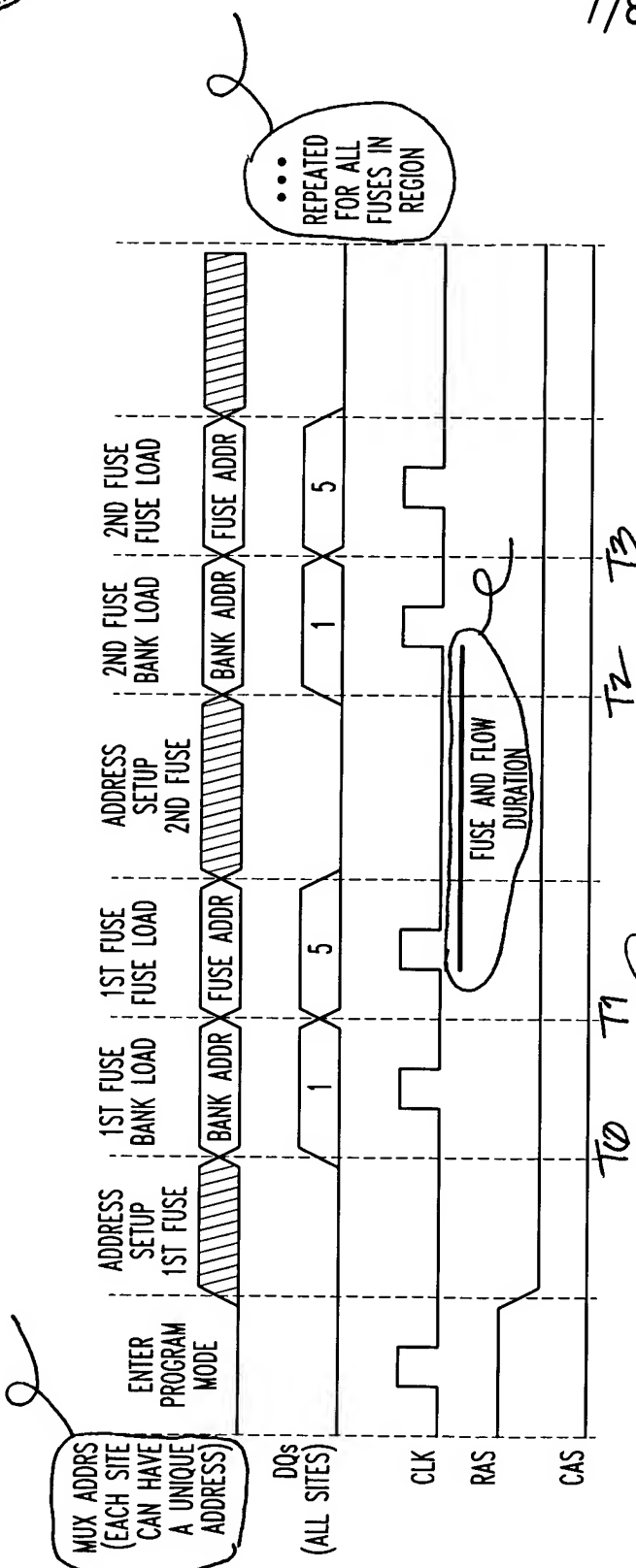


Fig. 6



DQs FOR BANK AND FUSE ADDRESS LOADING

HEX	FUNCTION
3210	LOAD NOTHING
0000	LOAD BANK ADDRESS LEFT
0001	LOAD BANK ADDRESS RIGHT
0010	LOAD FUSE ADDRESS TOP LEFT
0101	LOAD FUSE ADDRESS TOP RIGHT
0110	LOAD FUSE ADDRESS BOTTOM LEFT
1001	LOAD FUSE ADDRESS BOTTOM RIGHT
1010	LOAD FUSE ADDRESS BOTTOM RIGHT

DEVICE LAYOUT

DEVICE BANK (FUSE REGION) 0		DEVICE BANK (FUSE REGION) 1		DEVICE BANK (FUSE REGION) 2		DEVICE BANK (FUSE REGION) 3	
DQs	3210	DQs	3210	DQs	3210	DQs	3210
0001	0001	0001	0001	0010	0010	0010	0010
0101	0101	1001	1001	0110	0110	1010	1010
	LOAD BANK ADDRESS		LOAD BANK ADDRESS		LOAD BANK ADDRESS		LOAD BANK ADDRESS
	LOAD FUSE ADDRESS		LOAD FUSE ADDRESS		LOAD FUSE ADDRESS		LOAD FUSE ADDRESS

Fig. 7